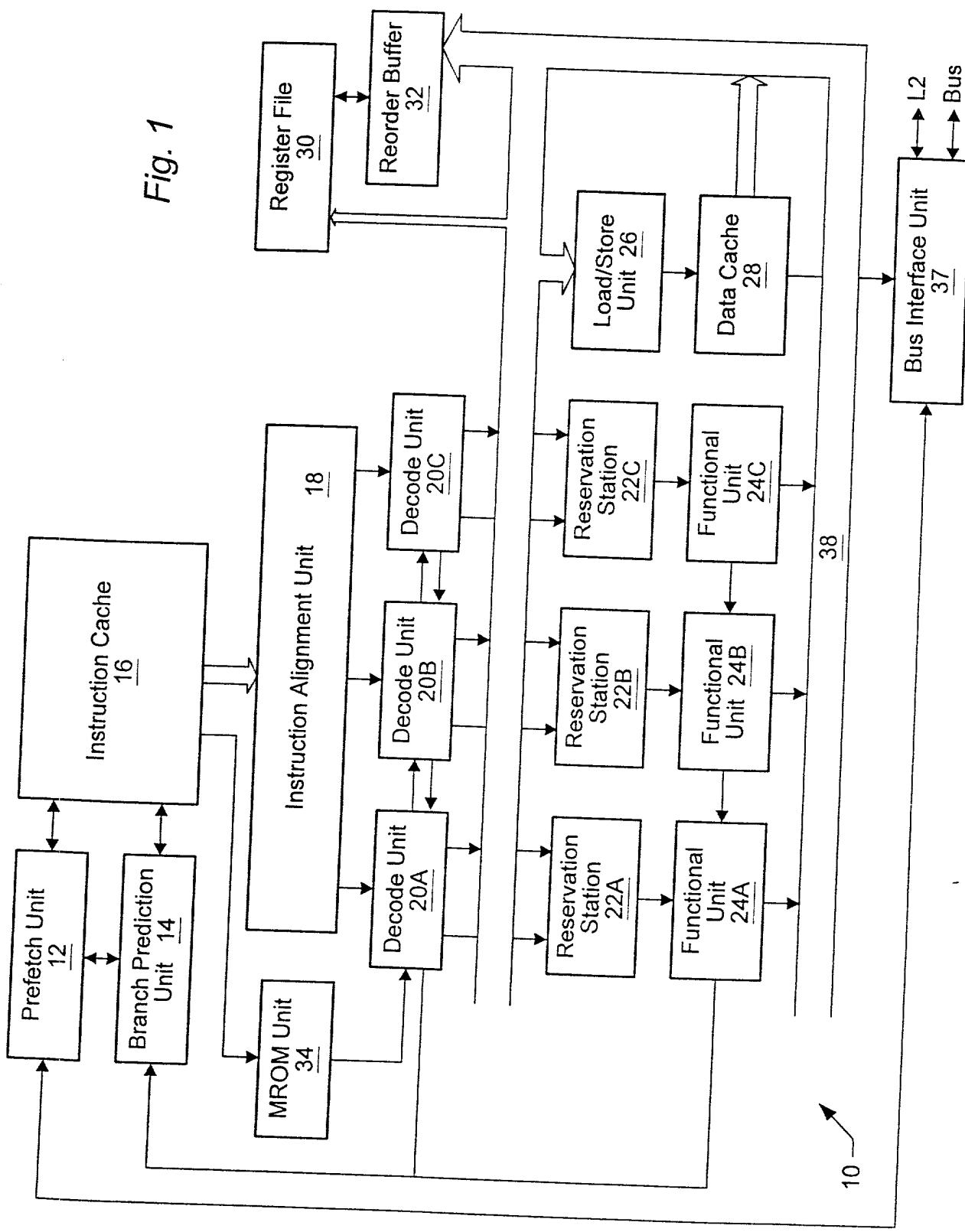


Fig. 1



10 →

38

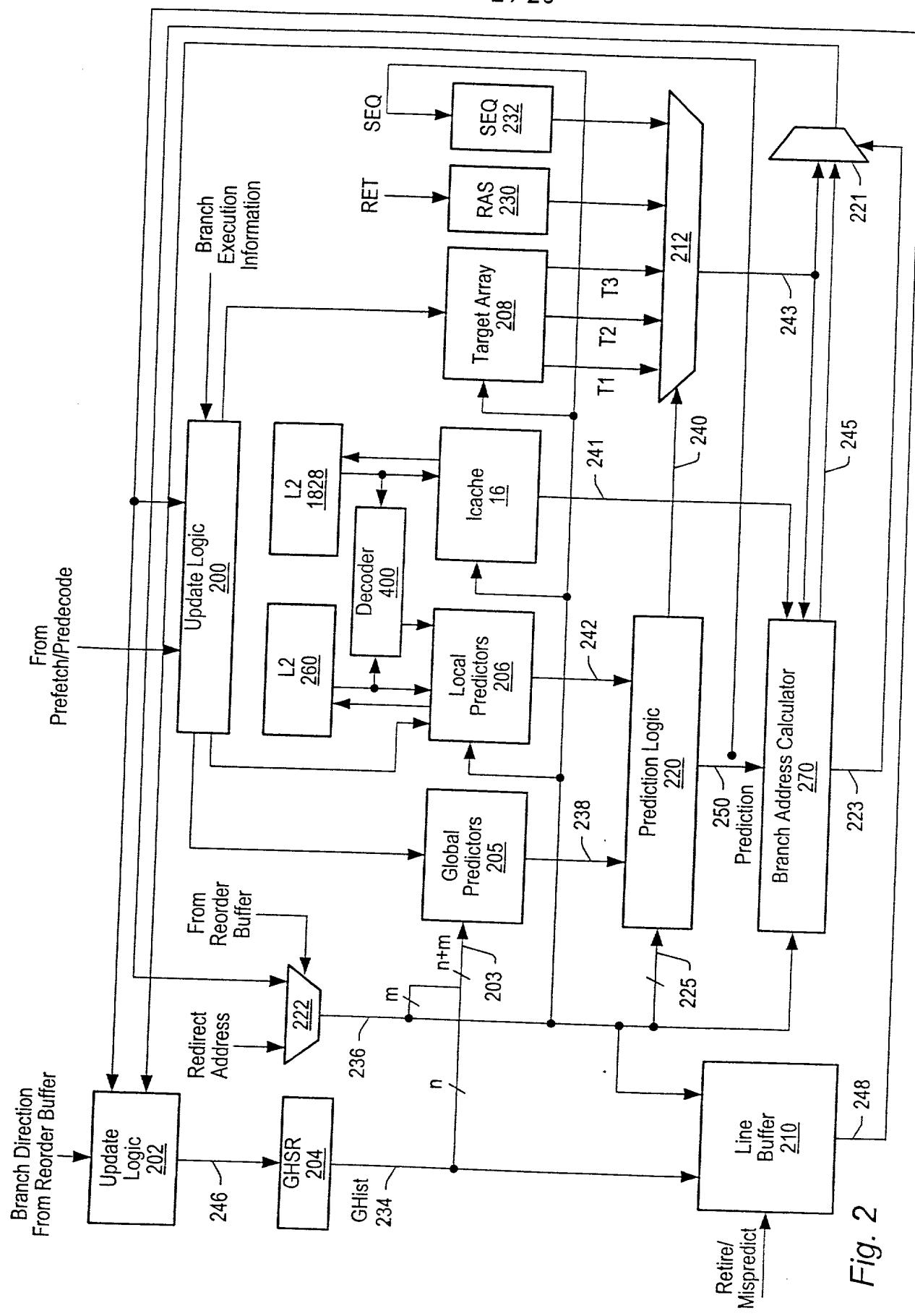


Fig. 2

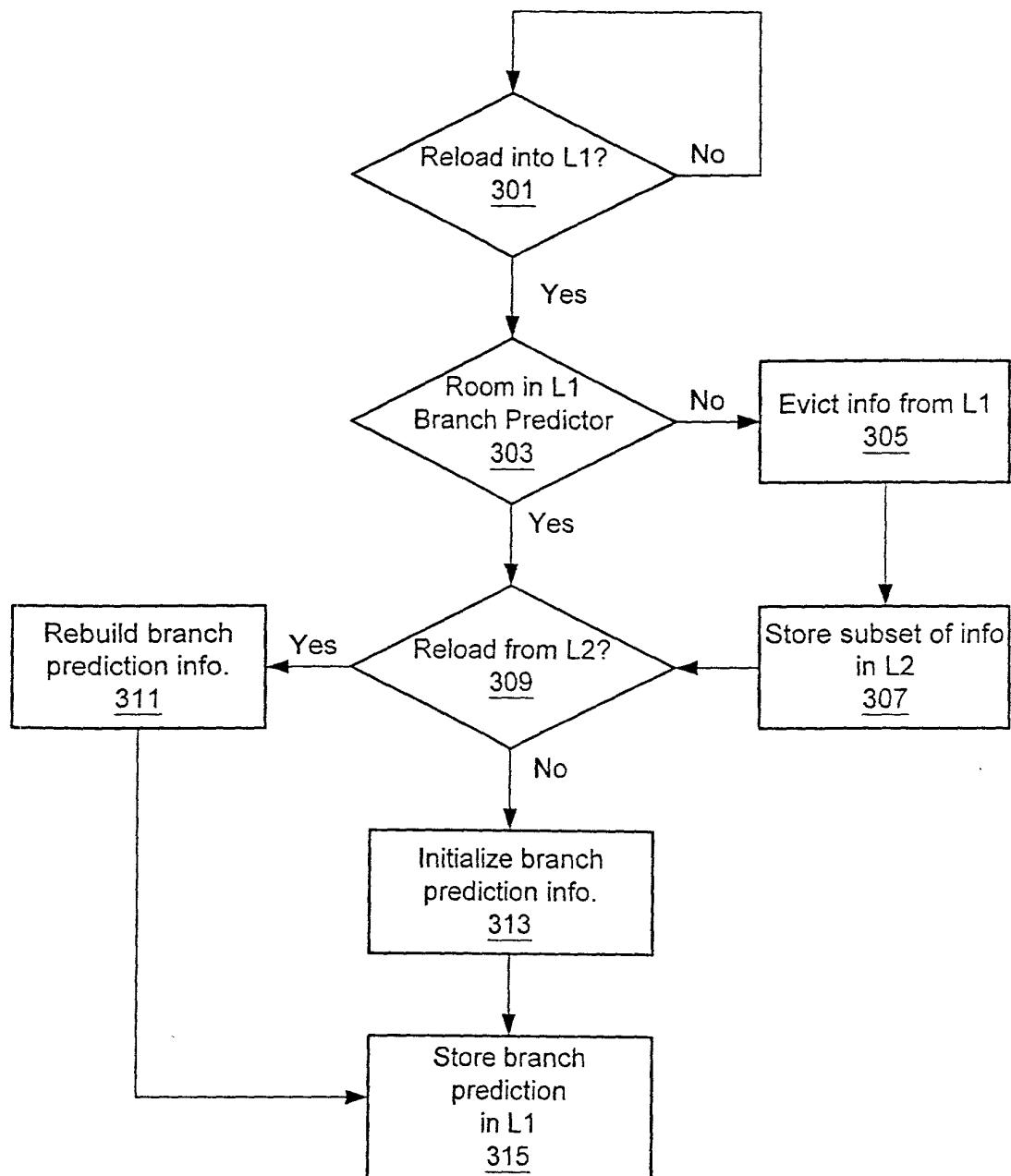


Fig. 3

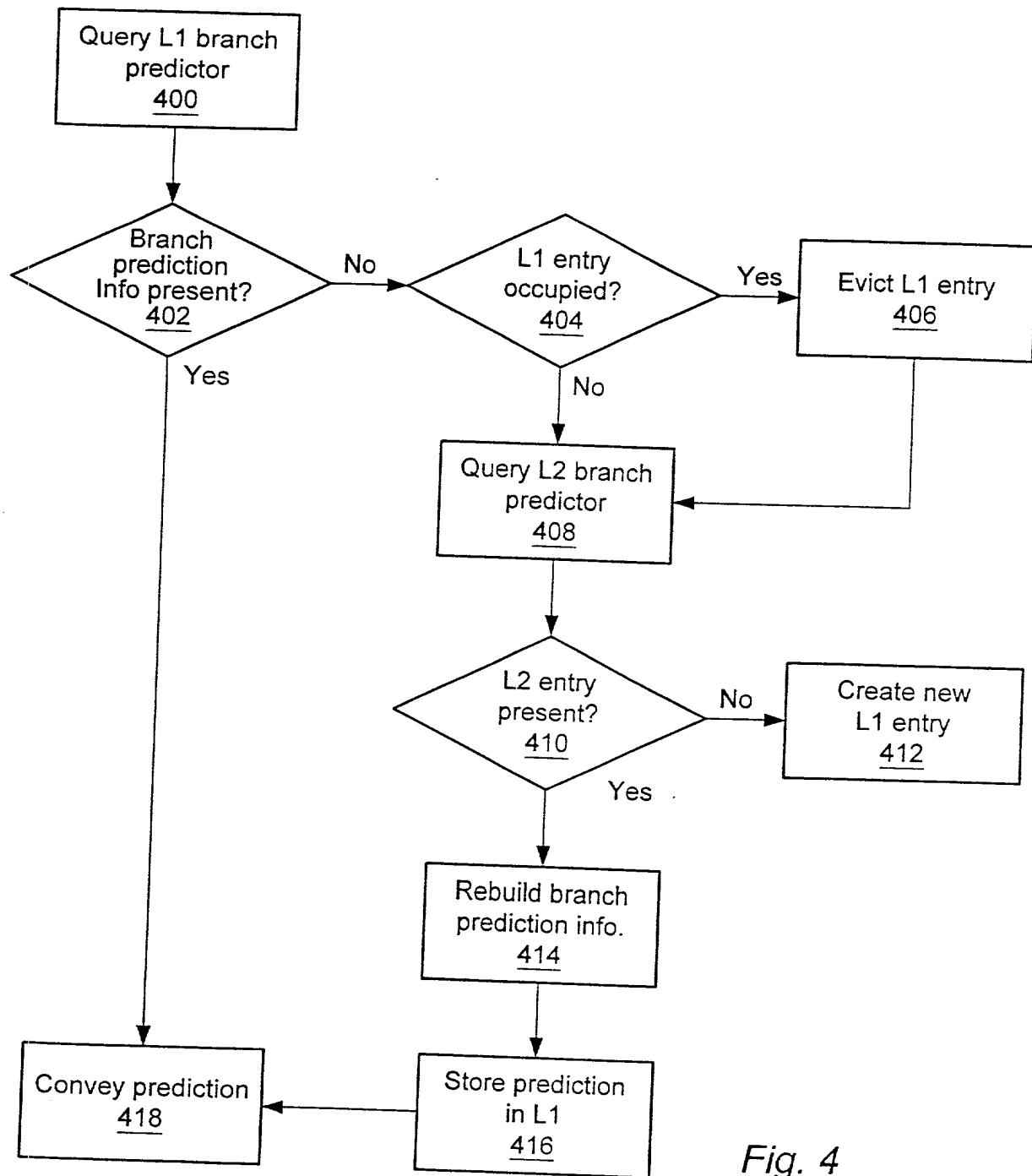
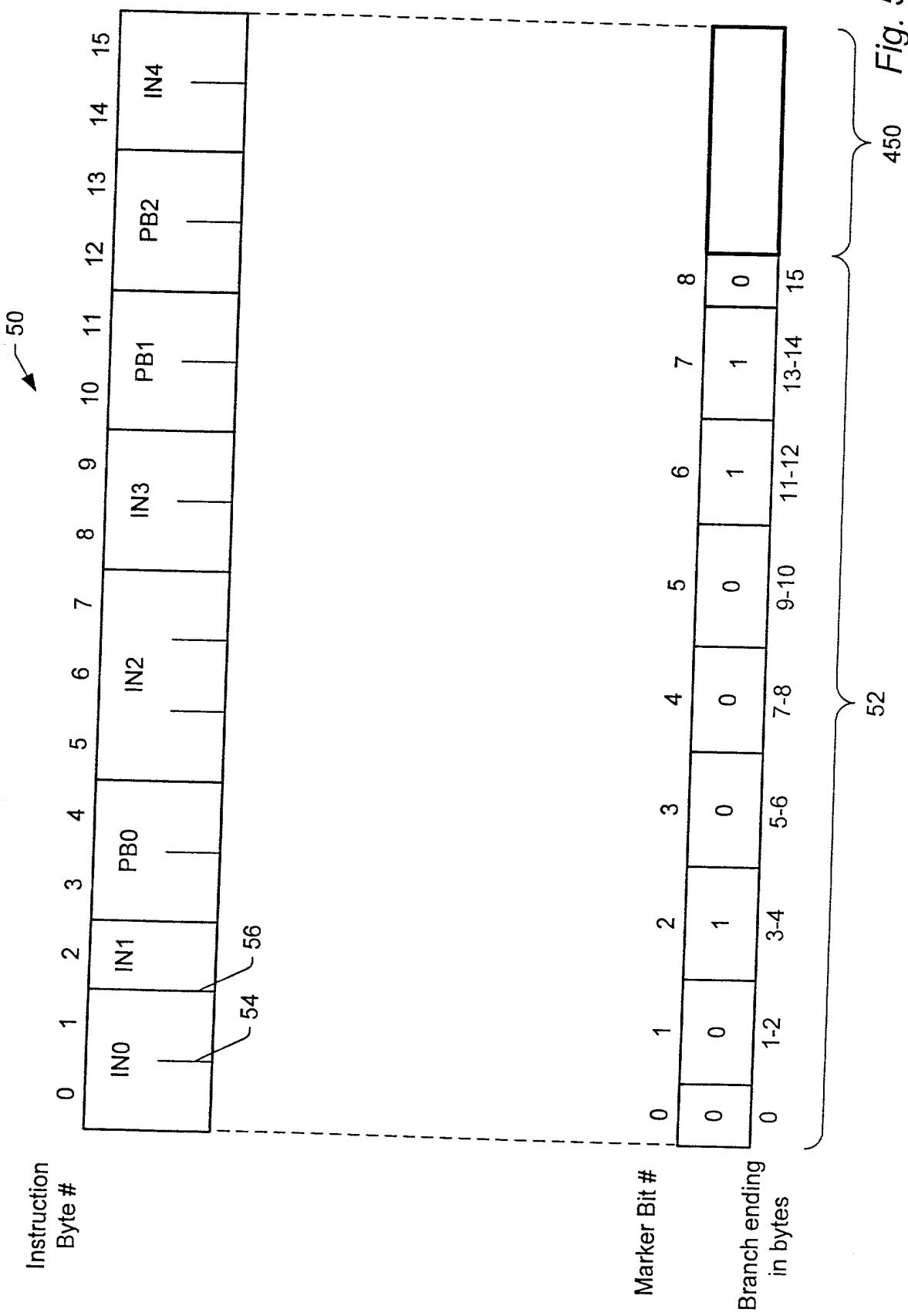


Fig. 4



Marker Bit #	0	1	2	3	4	5	6	7	8
Offset	<=0	<=1	<=3	<=5	<=7	<=9	<=11	<=13	<=15
602	604	606	608	610	612	614	616	618	618

Fig. 6

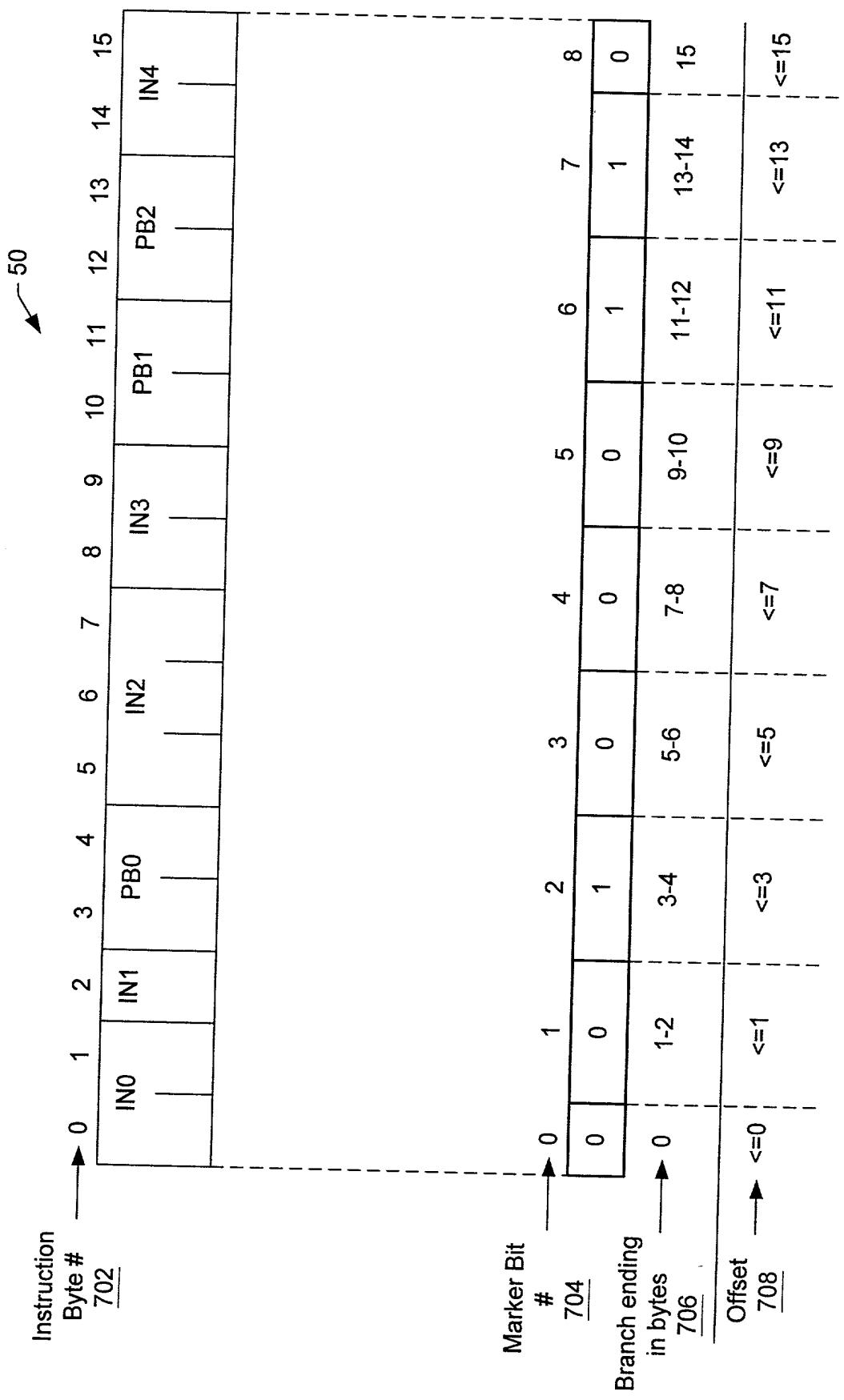


Fig. 7

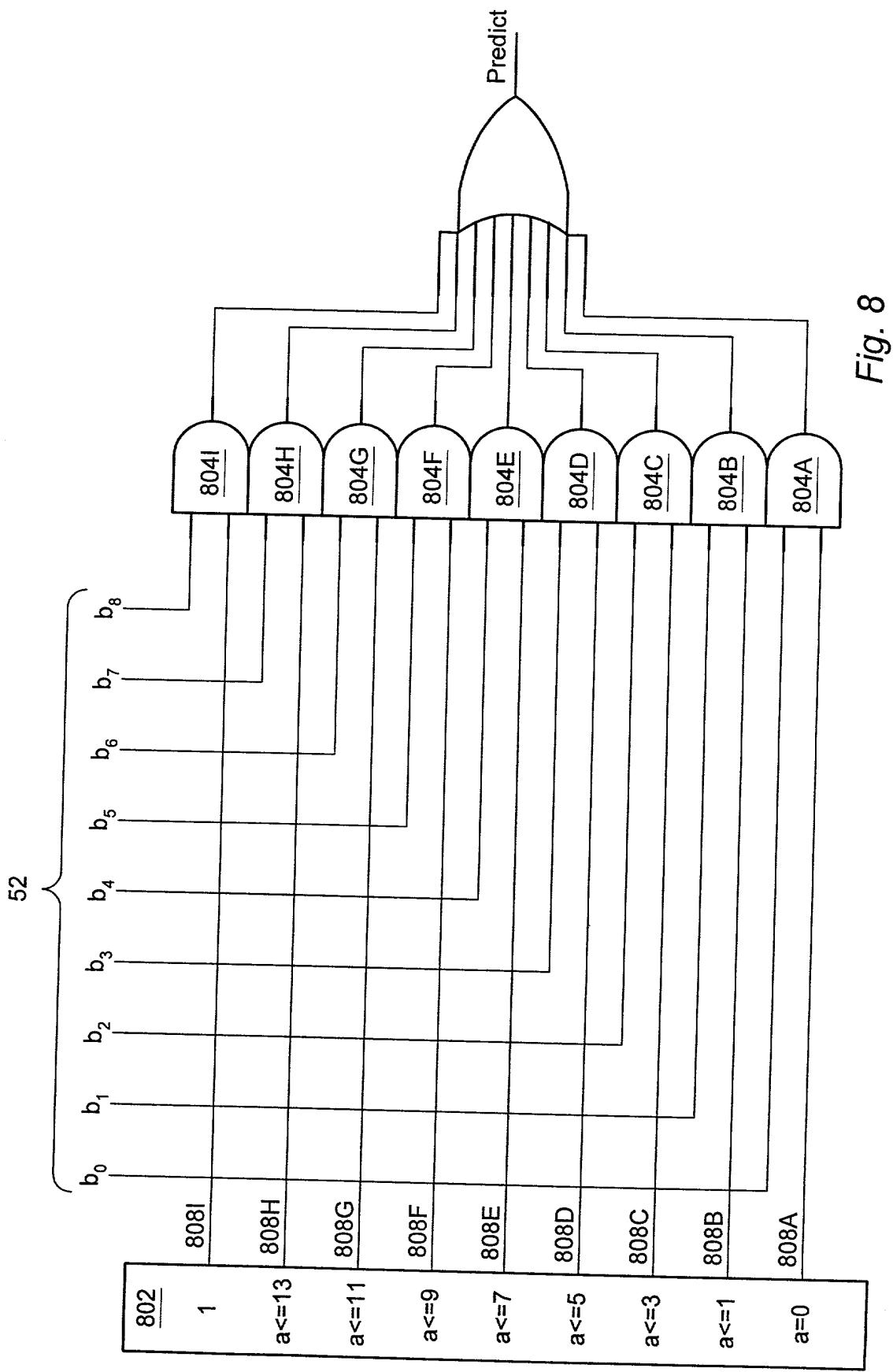


Fig. 8

9 / 26

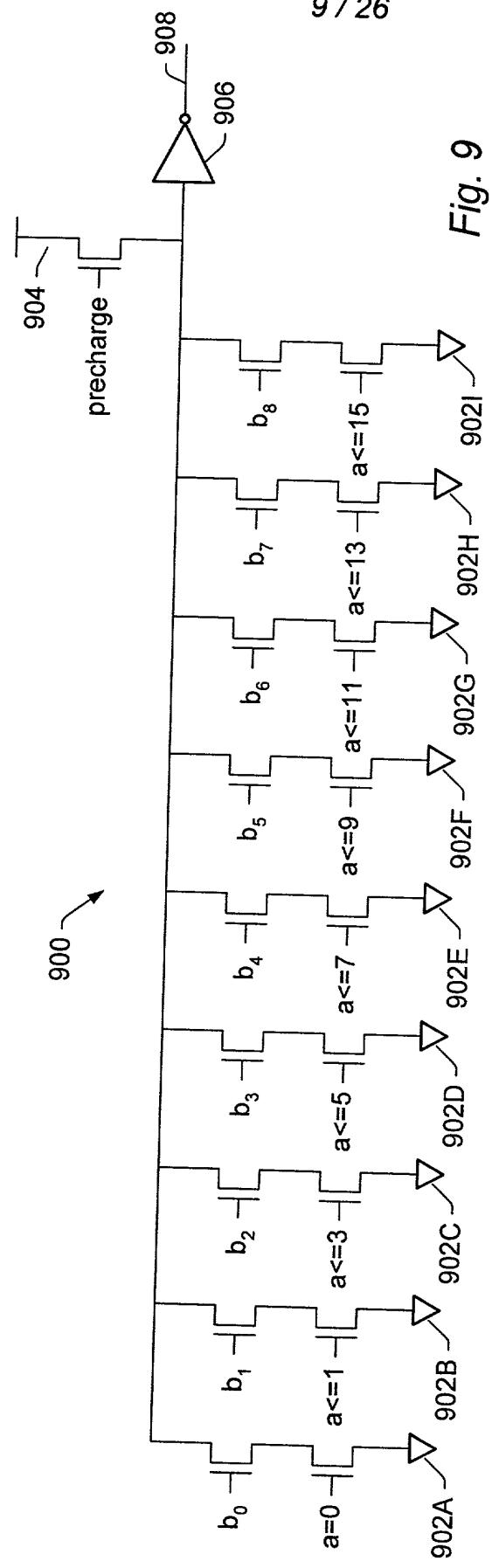


Fig. 9

10 / 26

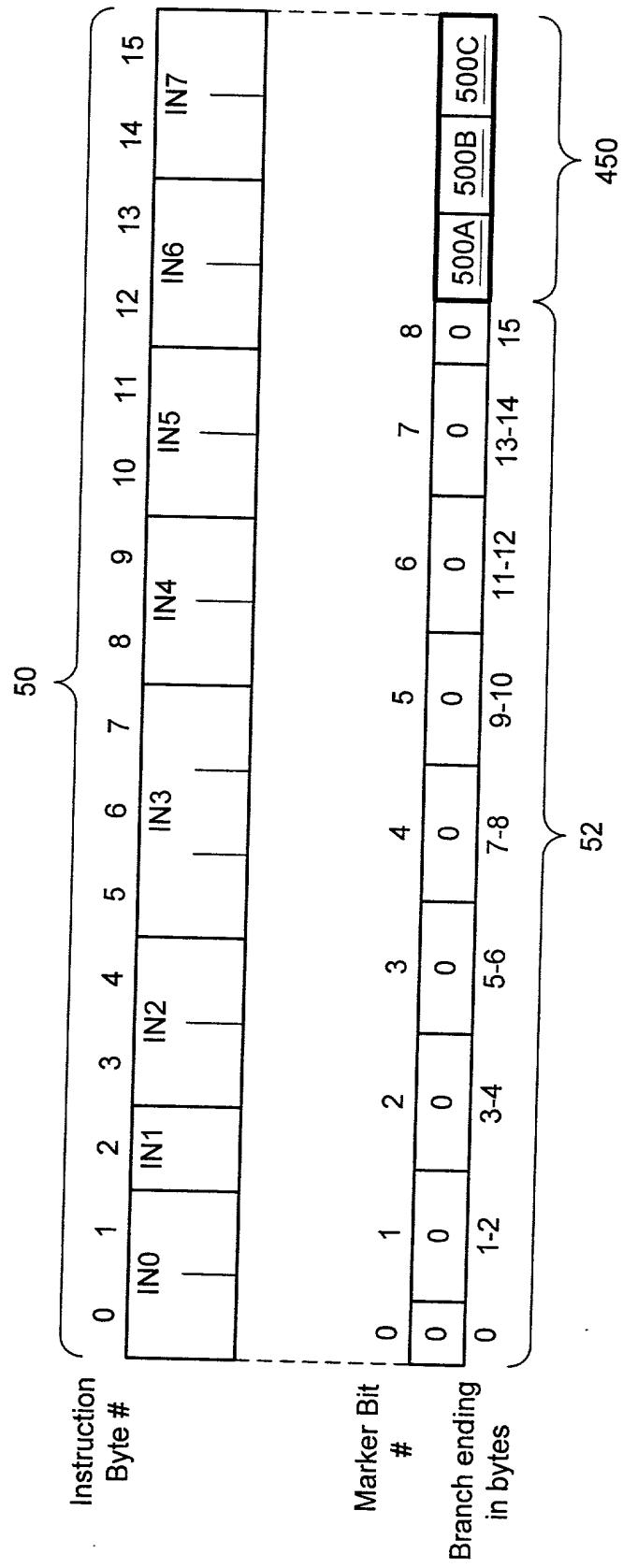


Fig. 10

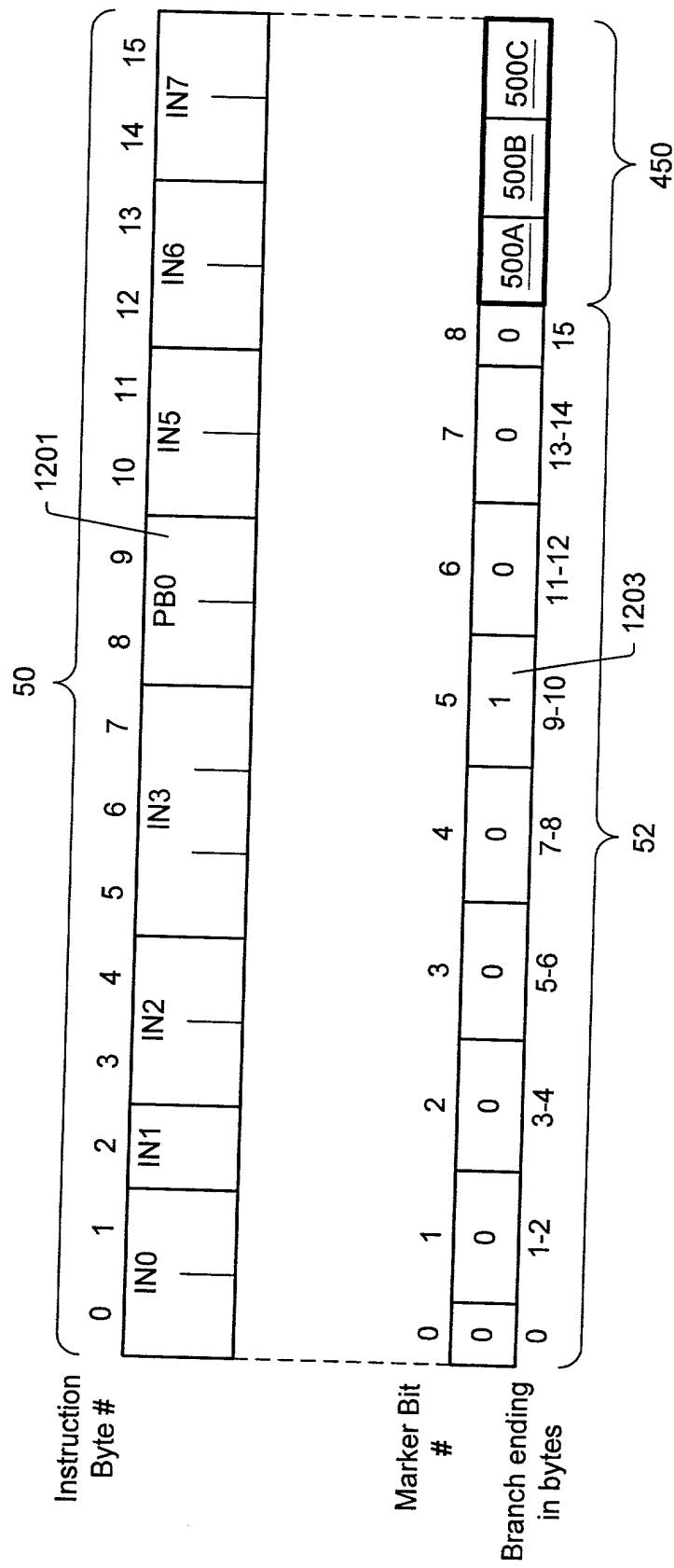
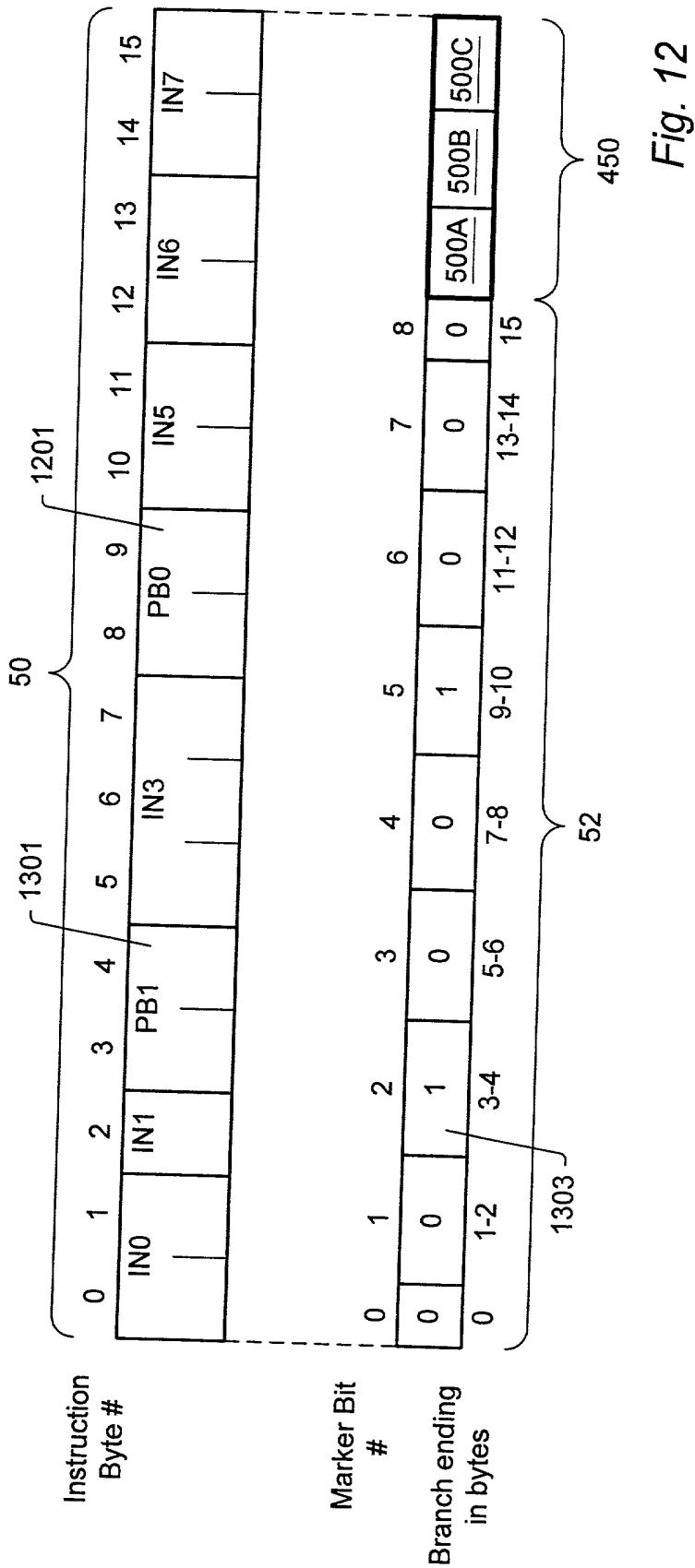


Fig. 11

12 / 26



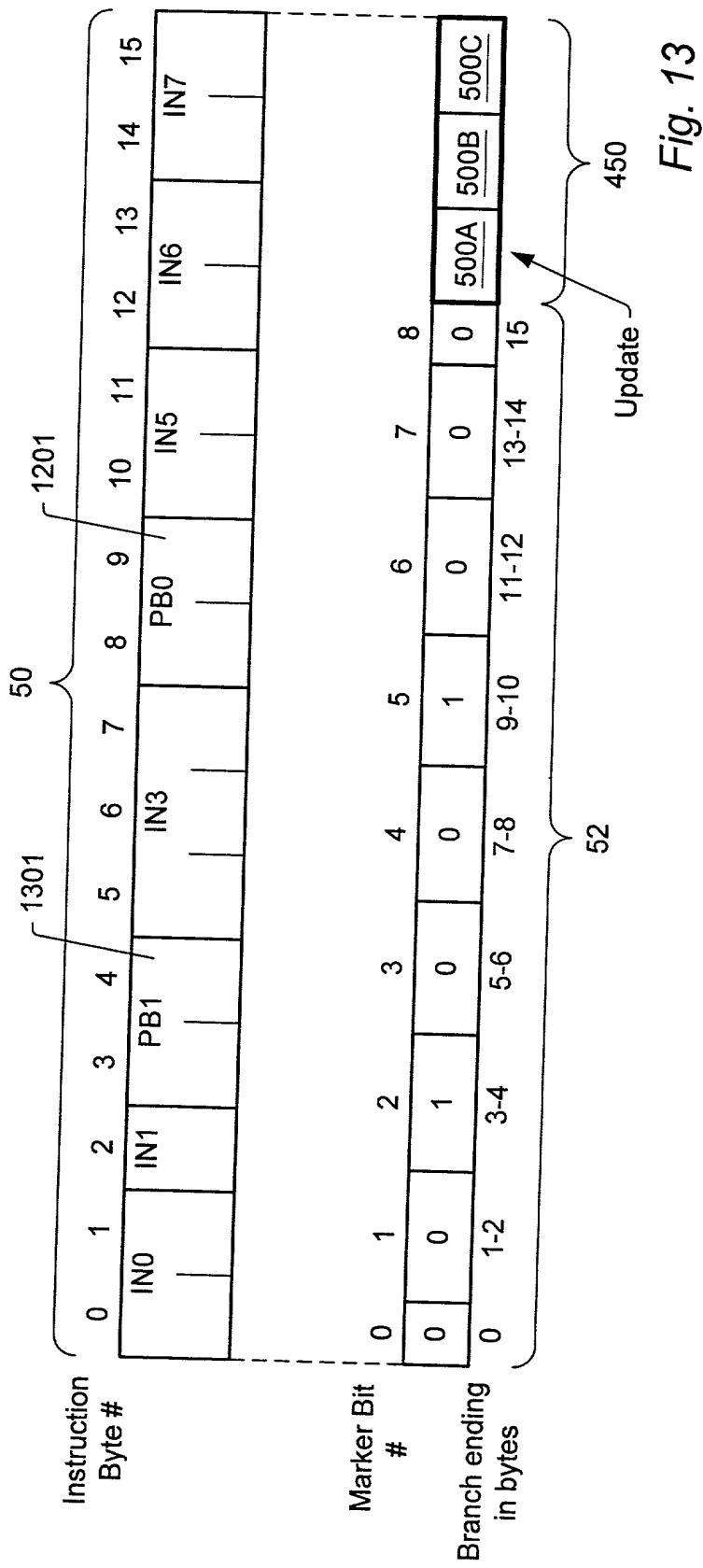


Fig. 13

14 / 26

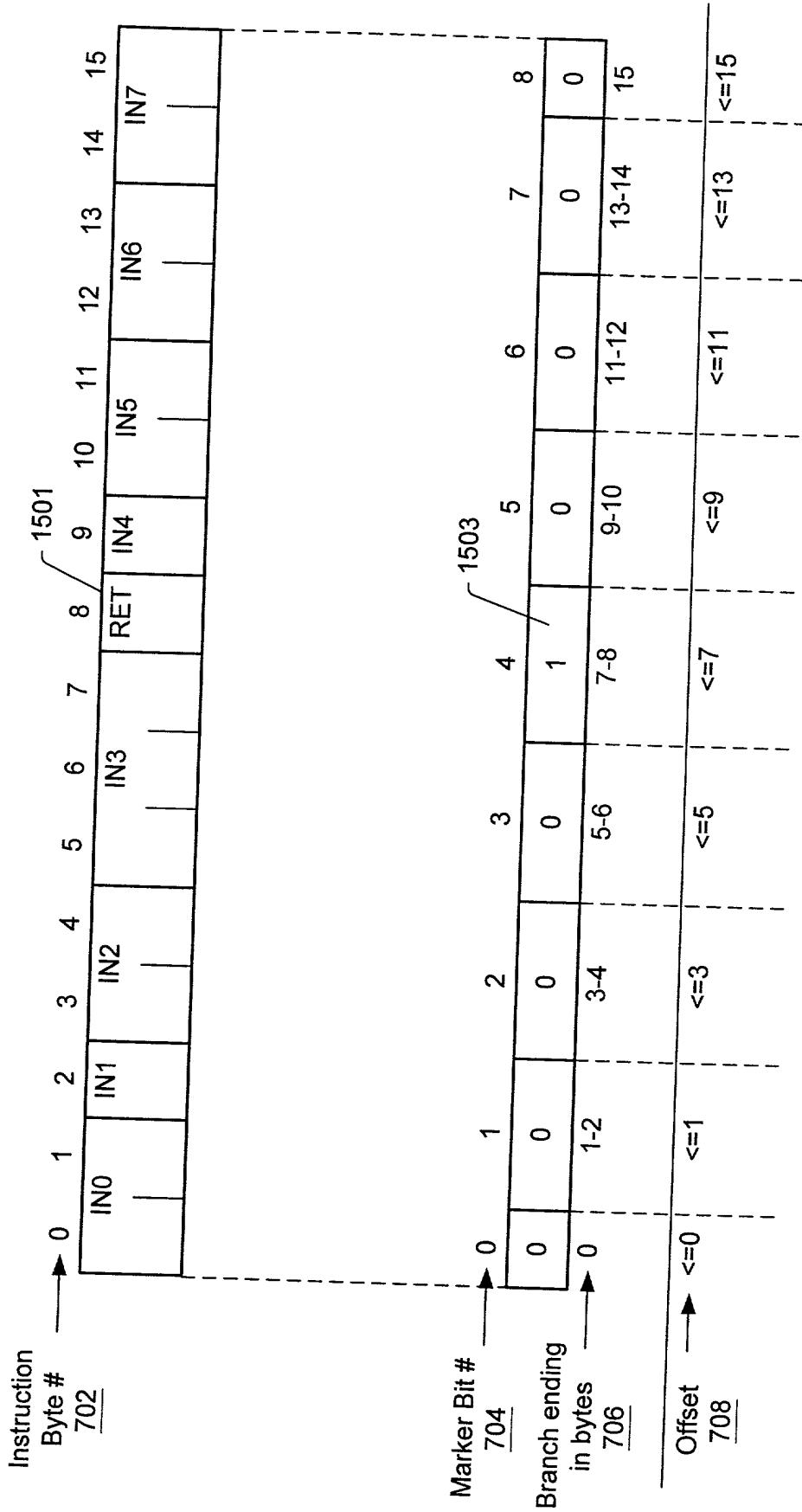


Fig. 14

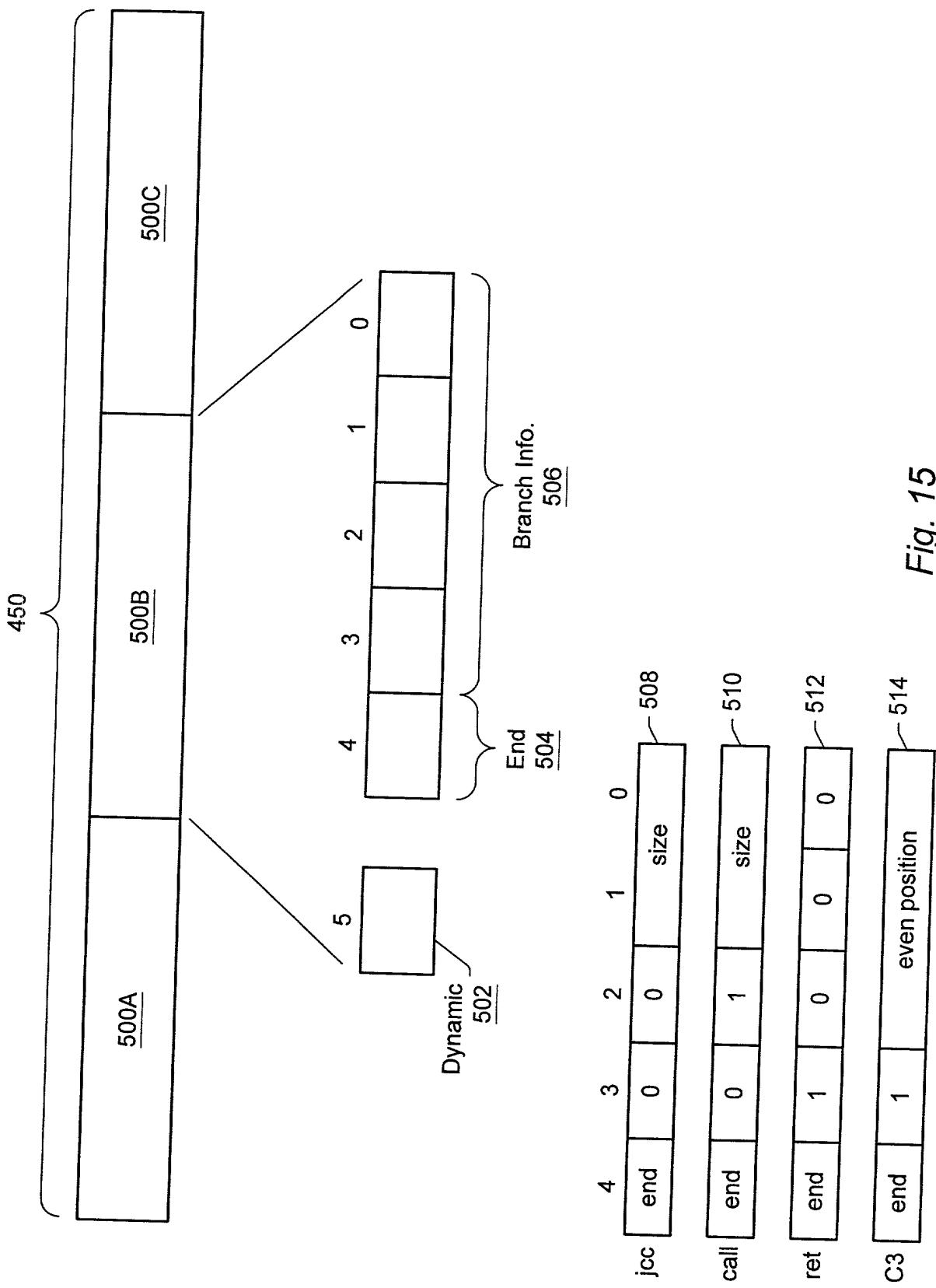


Fig. 15

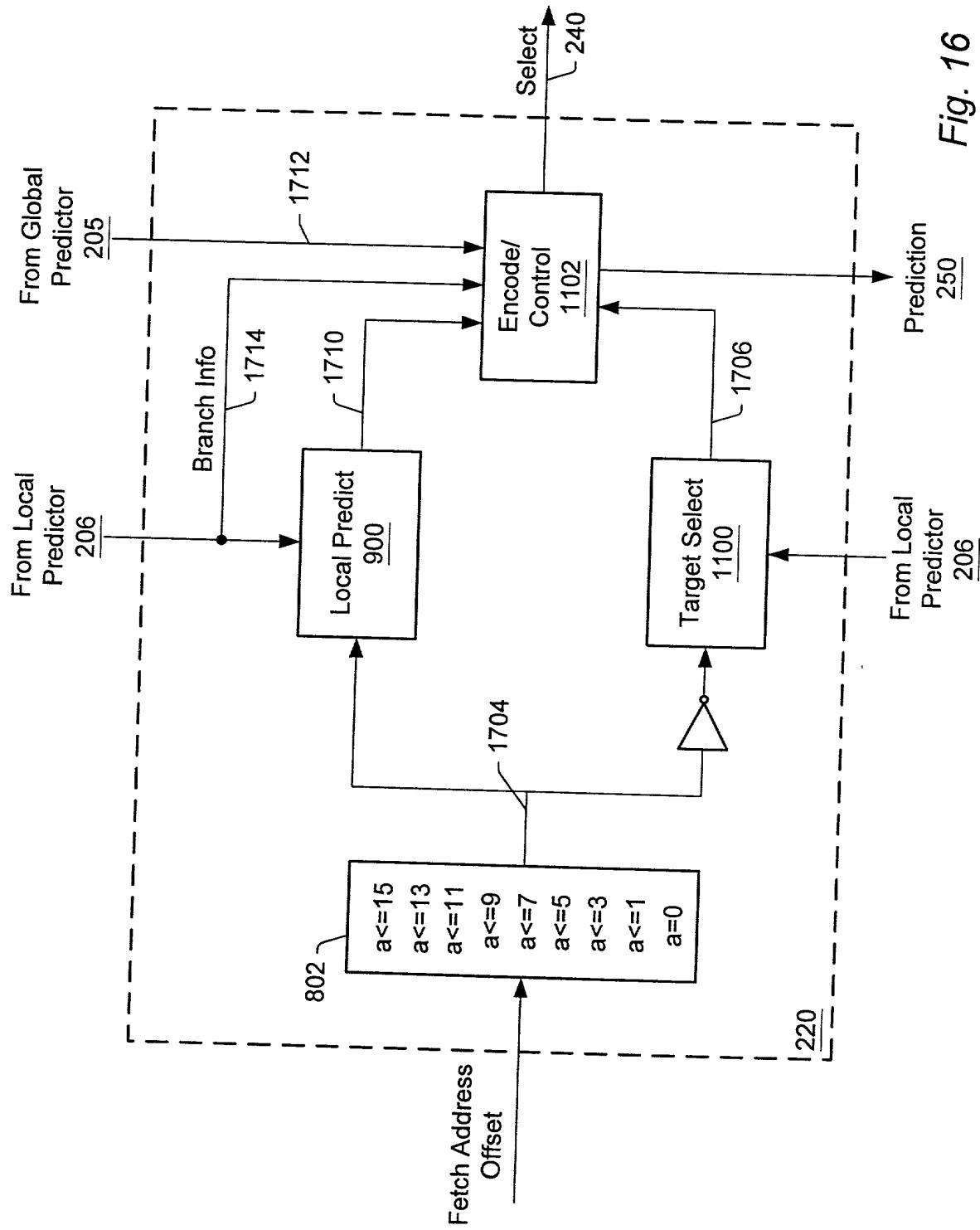


Fig. 16

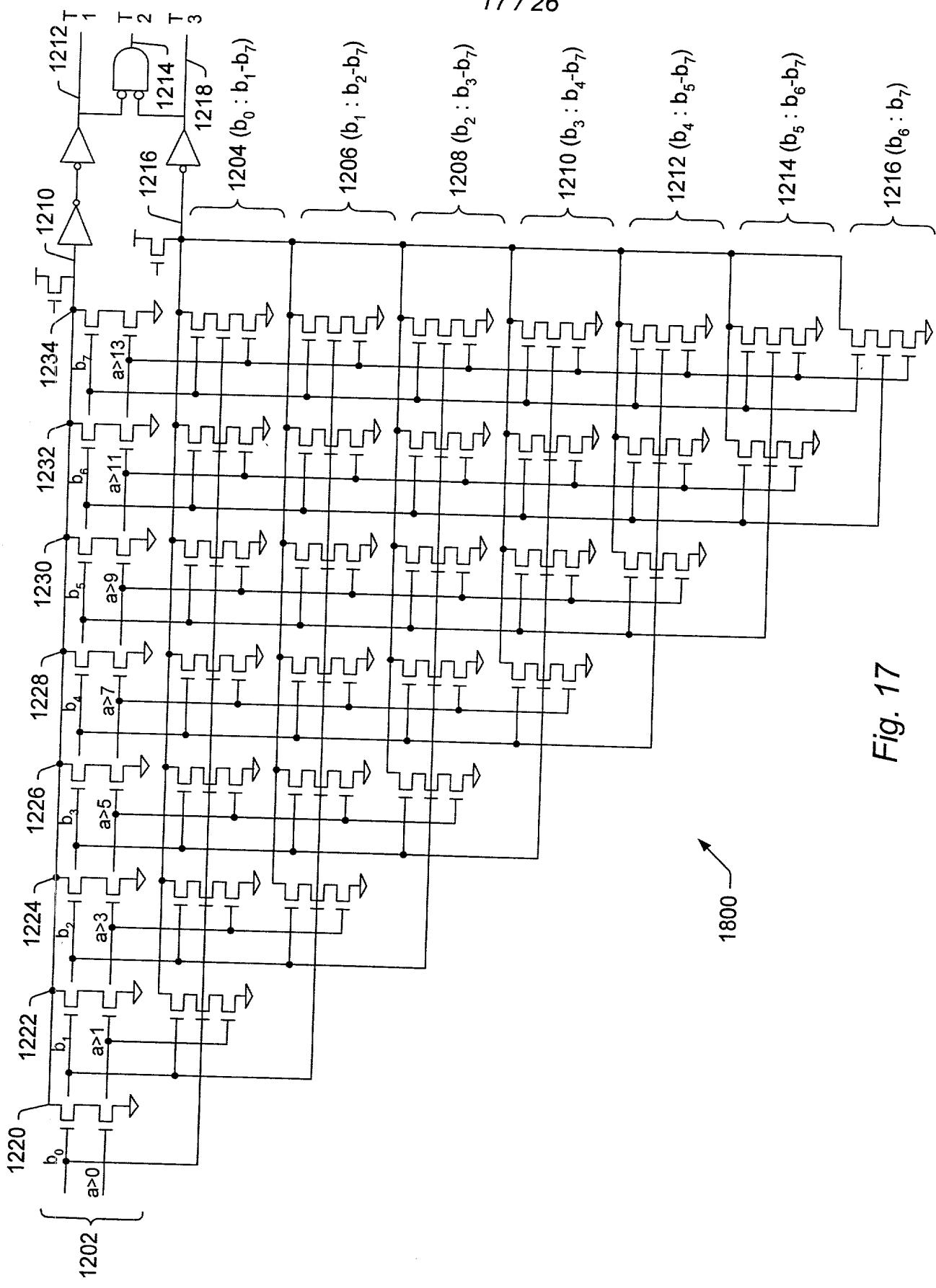


Fig. 17

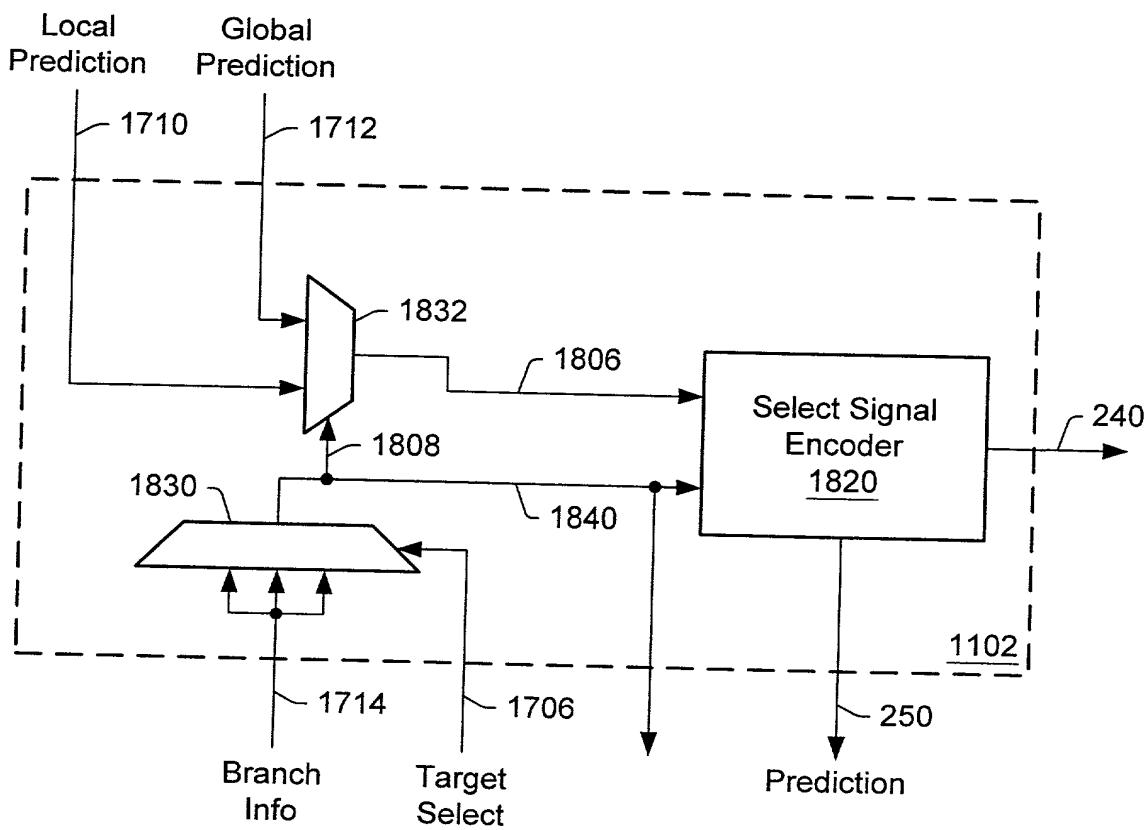


Fig. 18

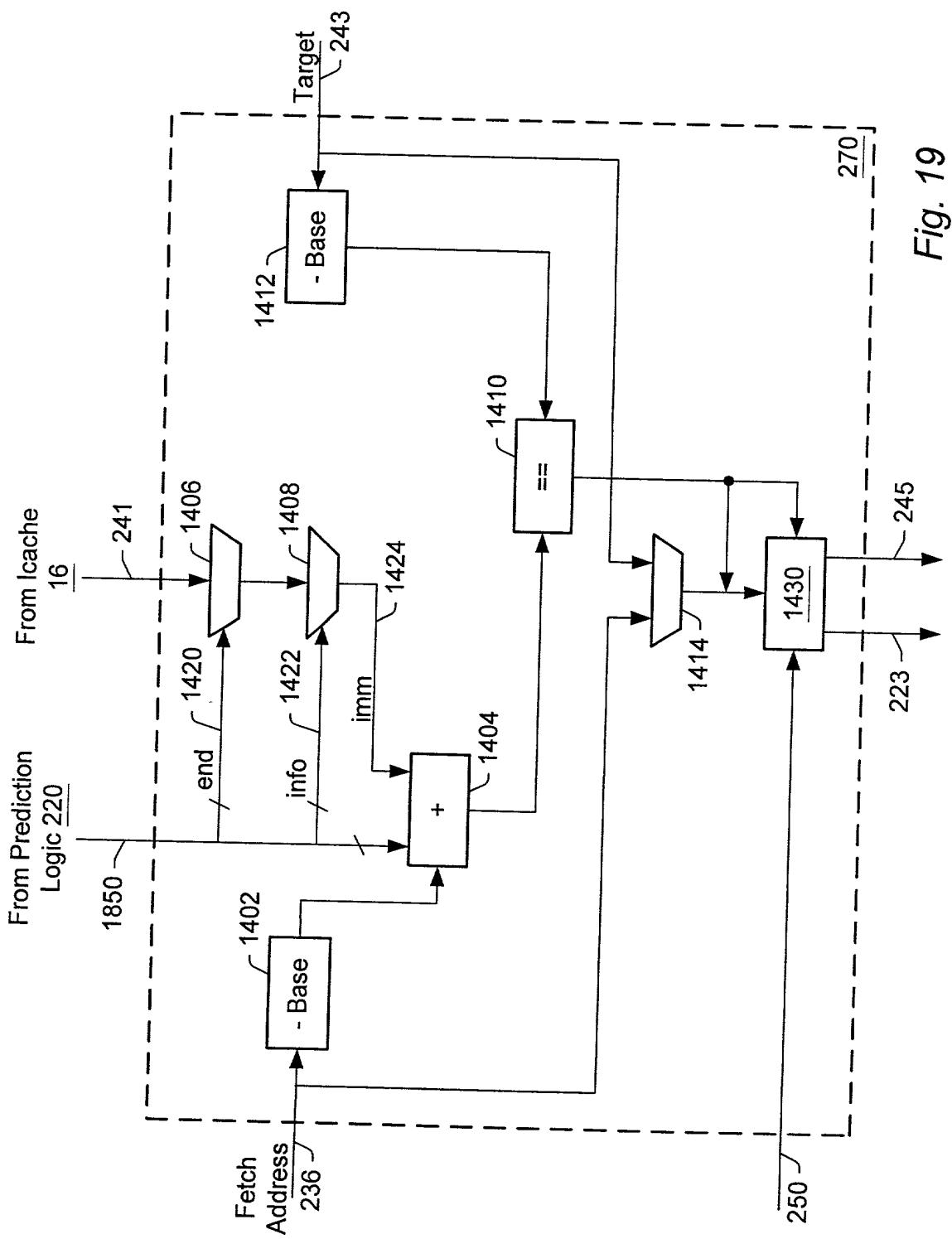


Fig. 19

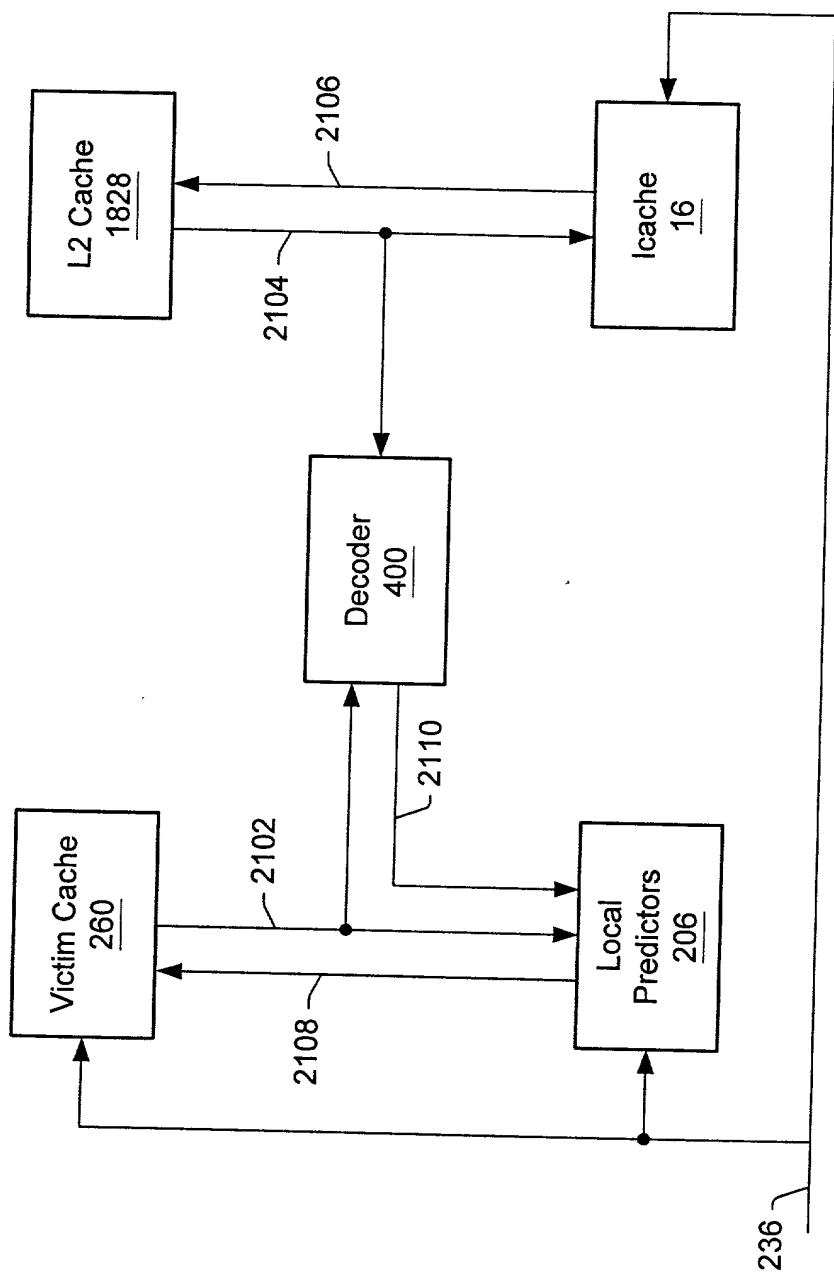


Fig. 20

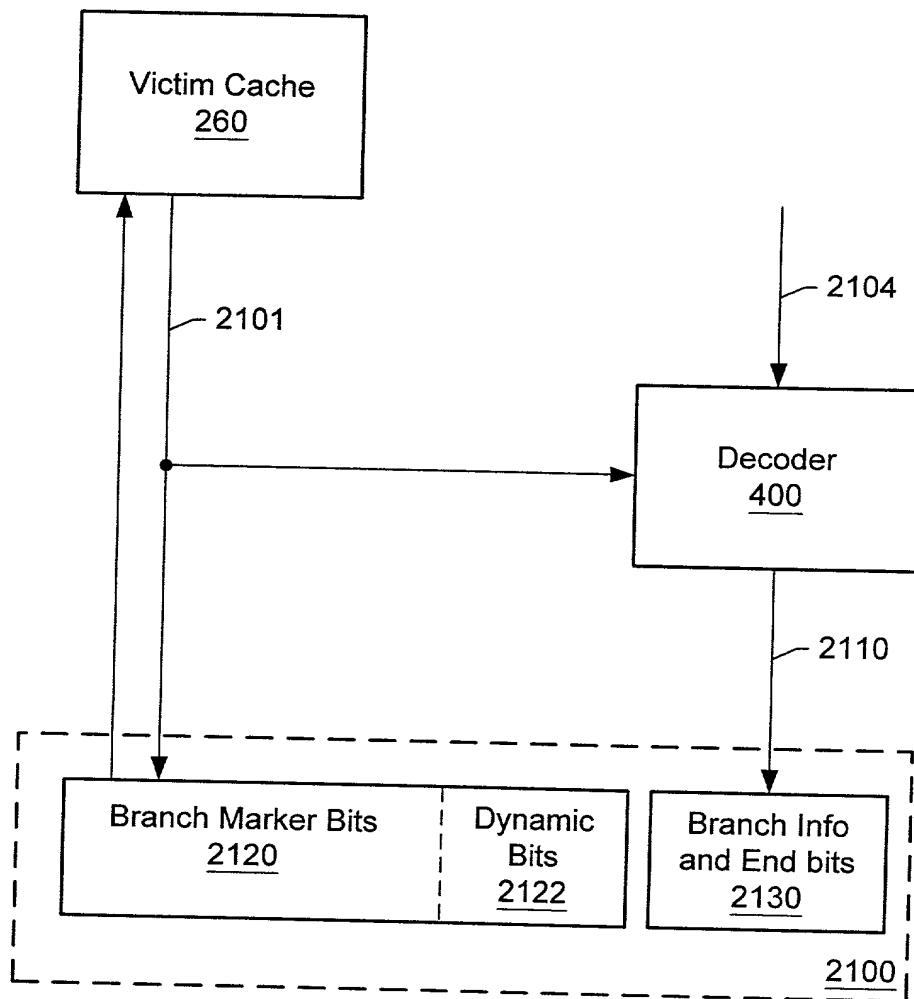


Fig. 21

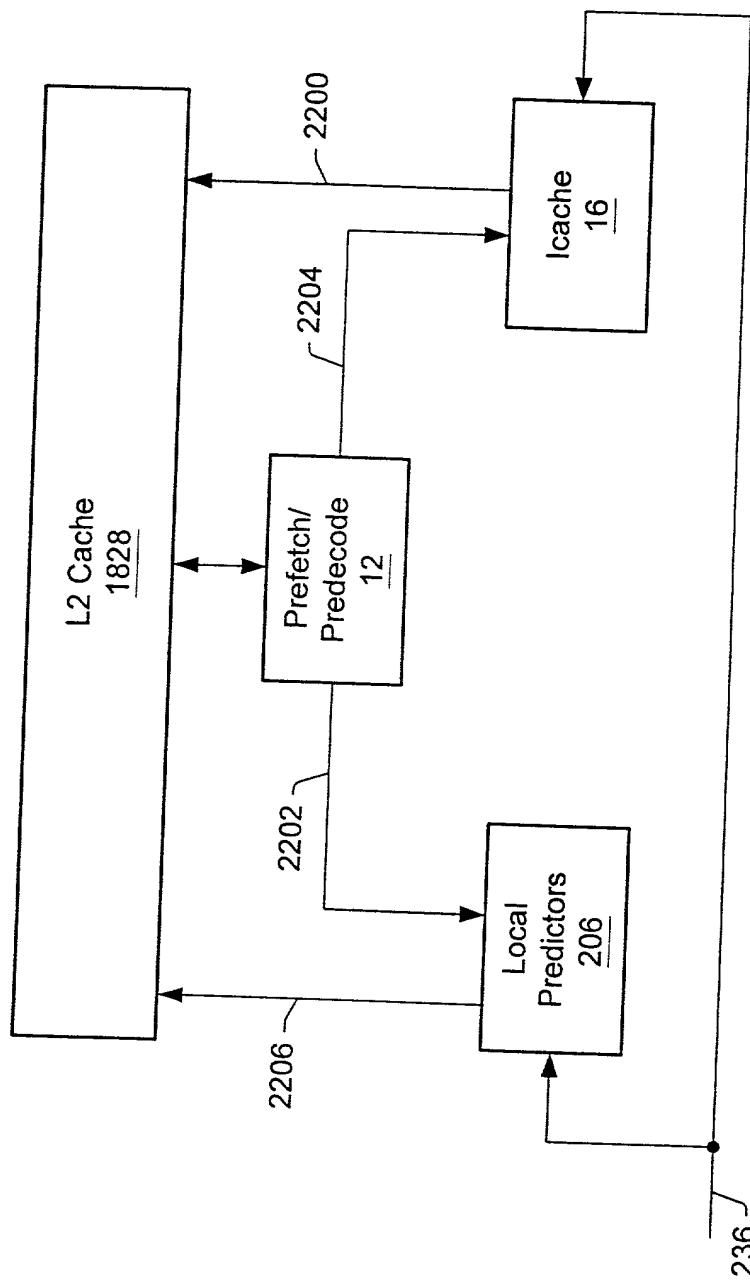
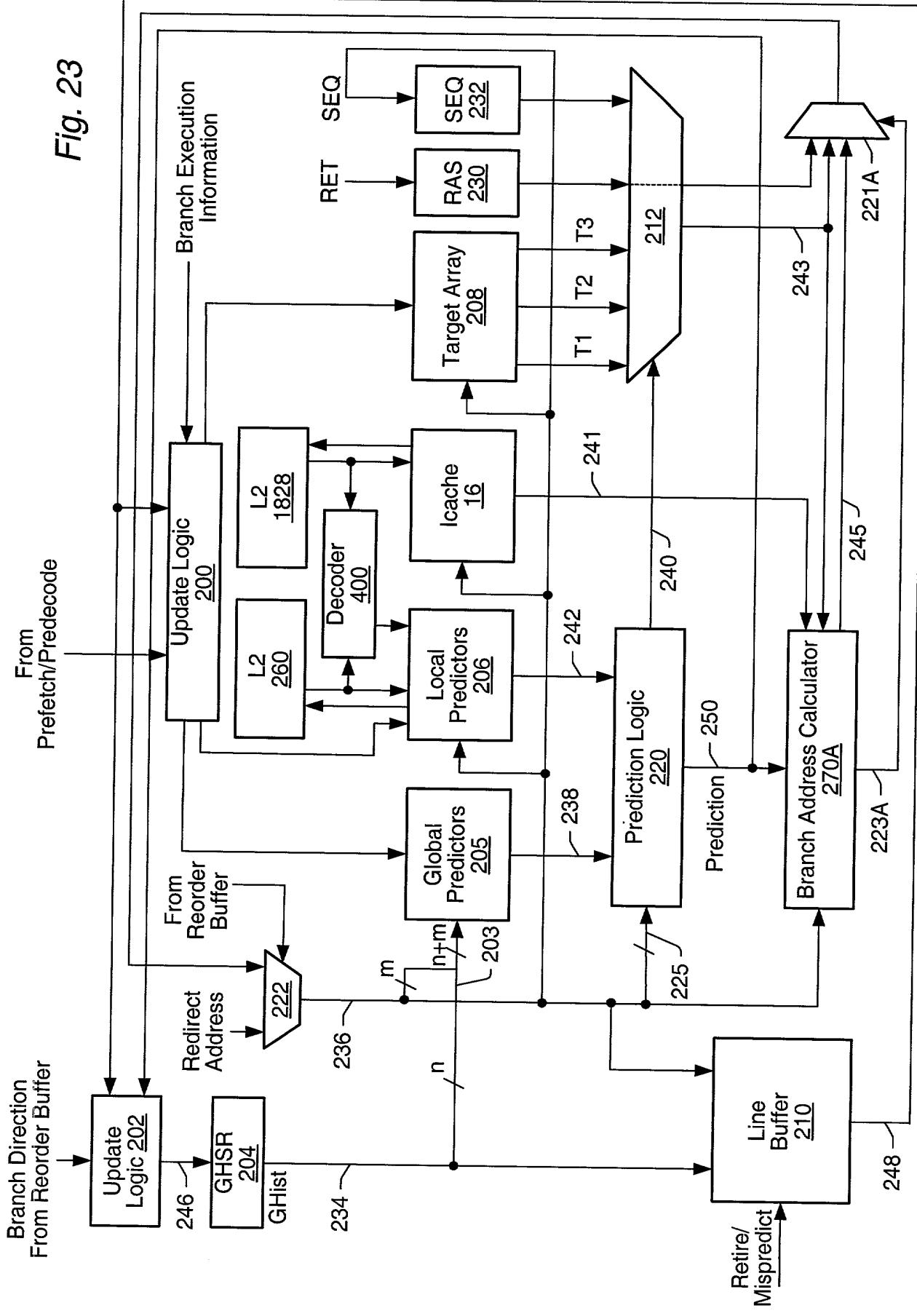


Fig. 22

Fig. 23



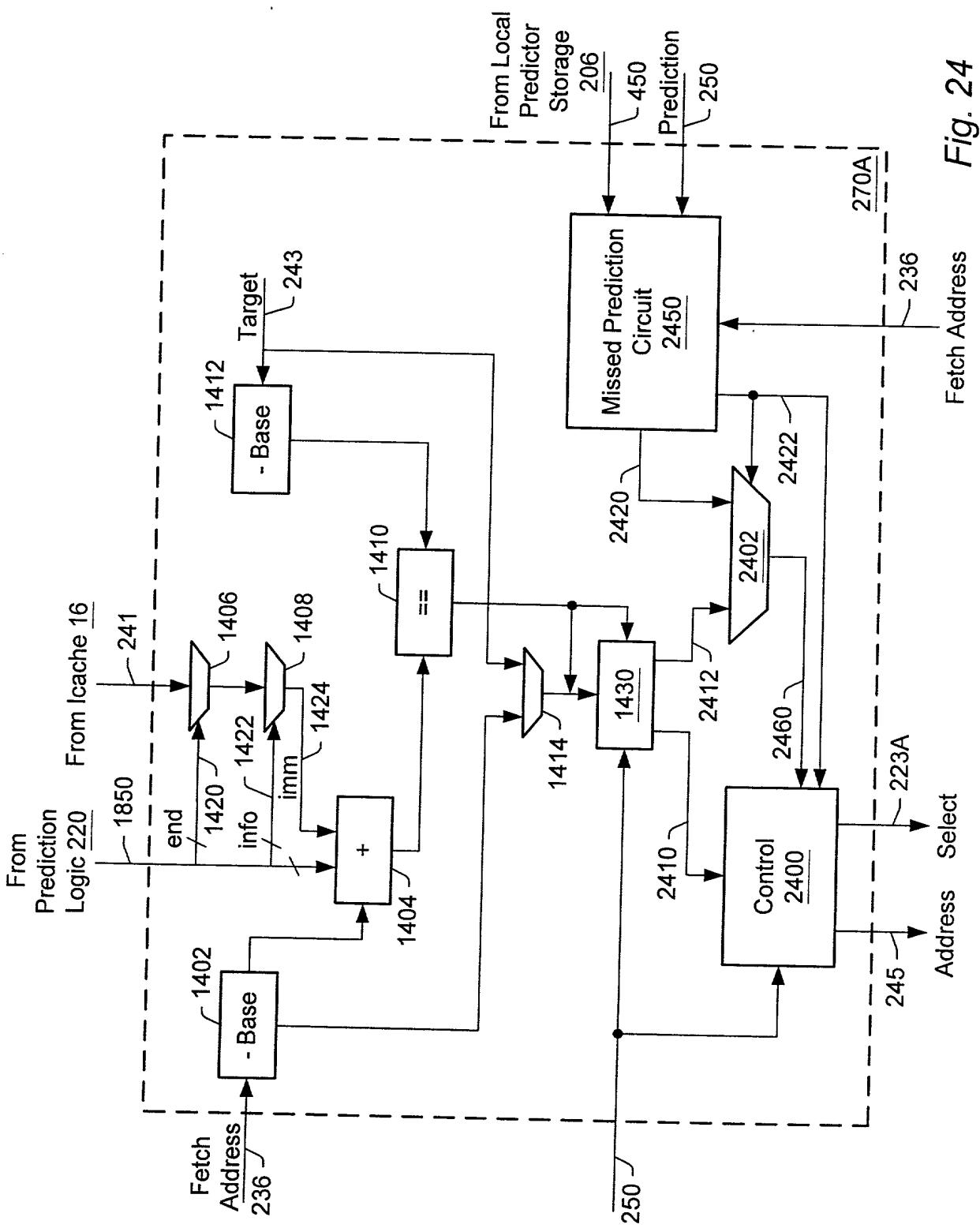


Fig. 24

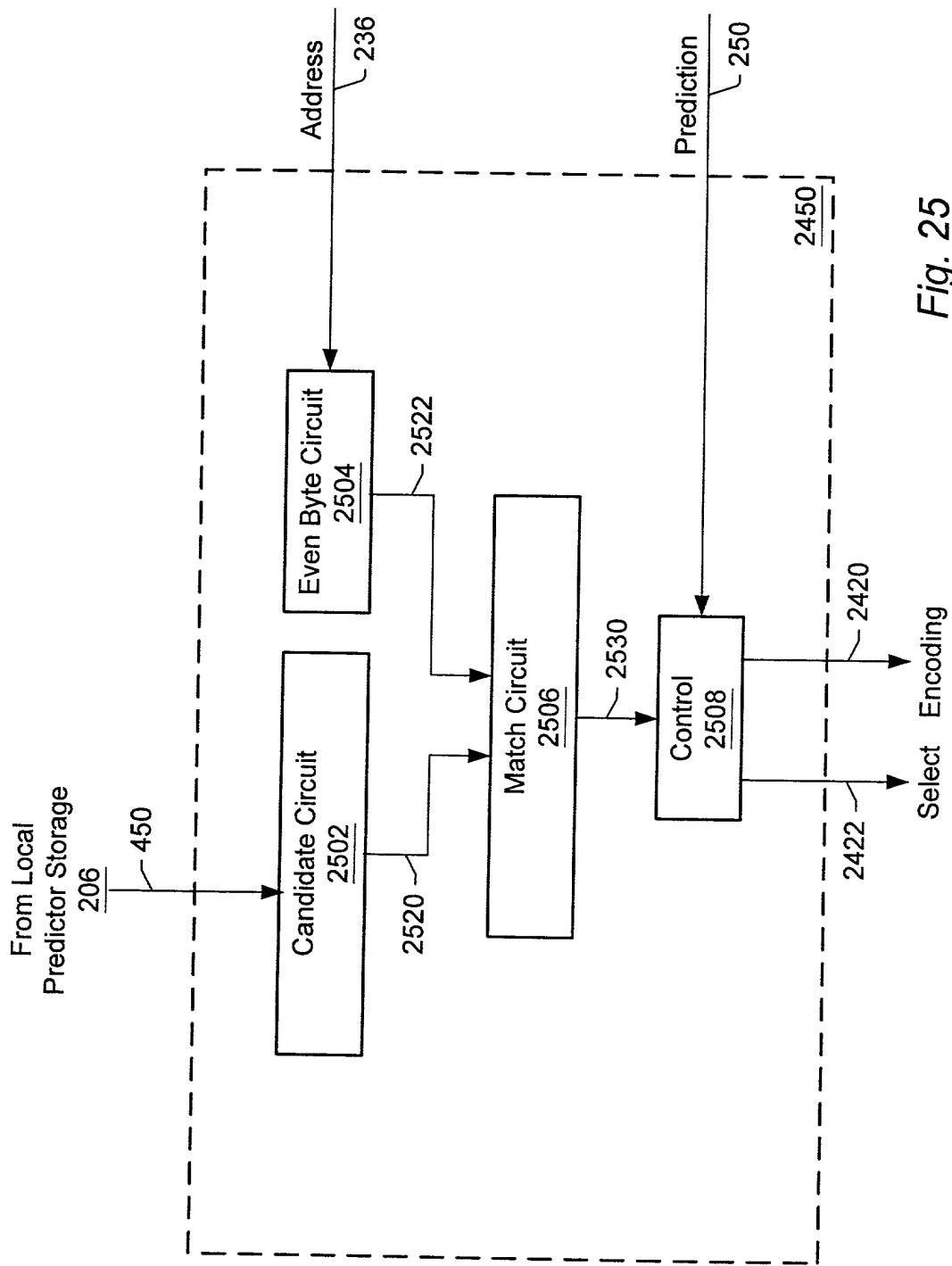


Fig. 25

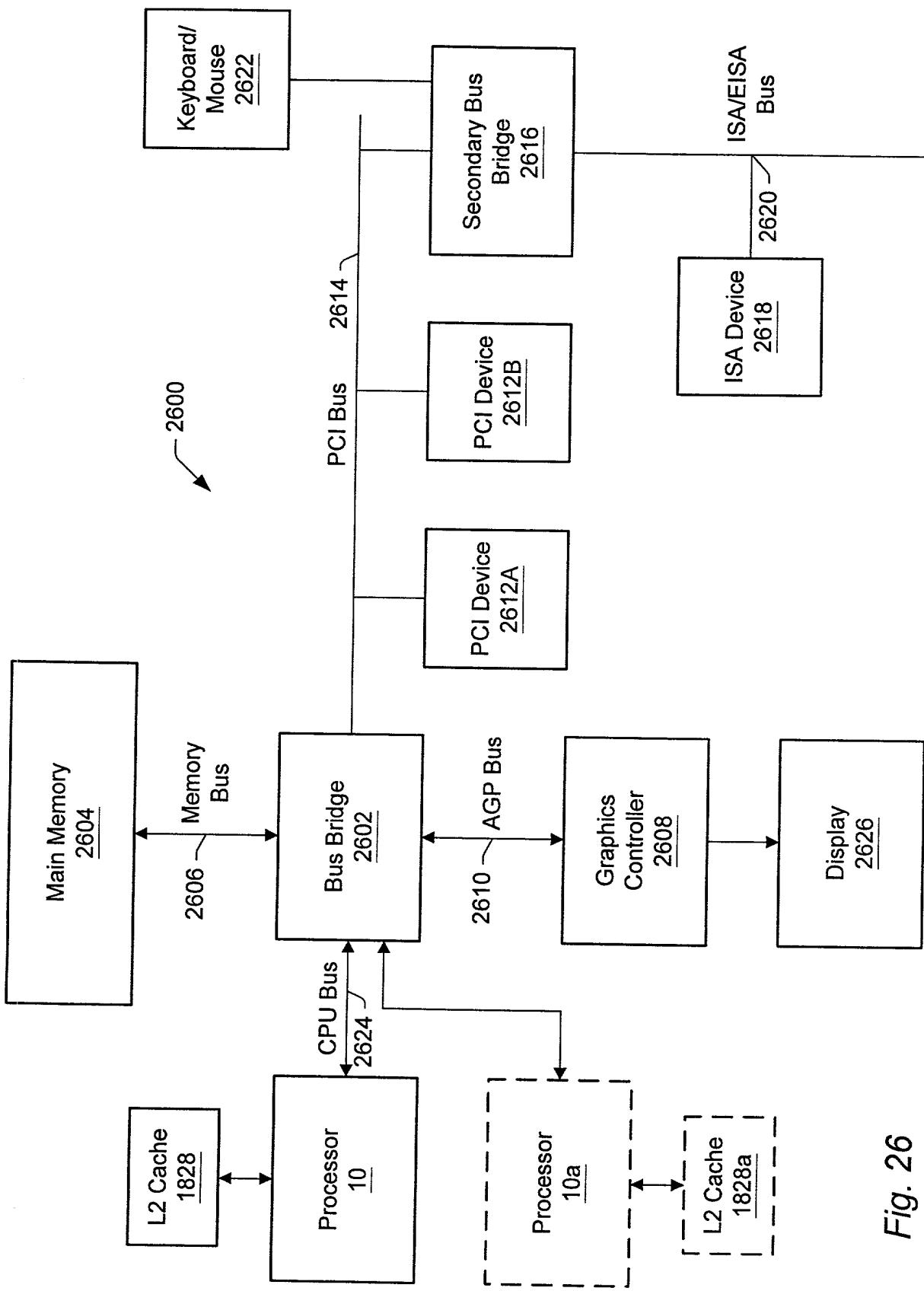


Fig. 26